

Fig. 1

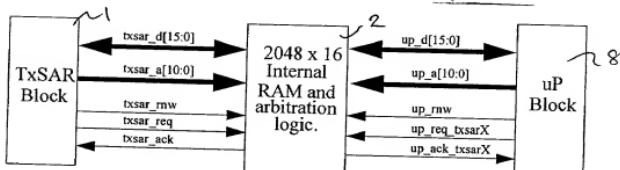


Fig. 2

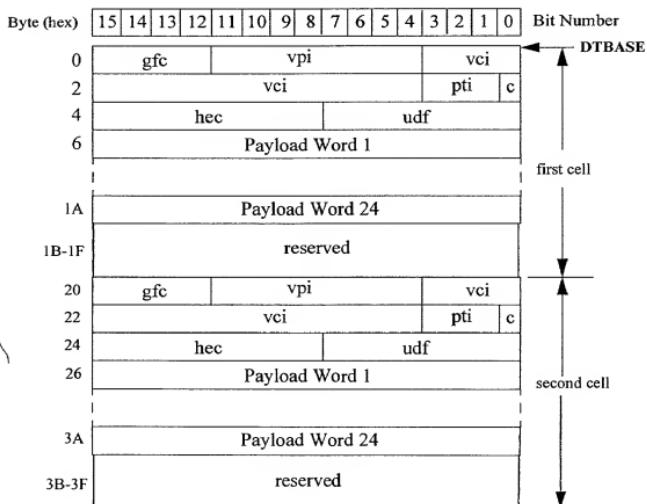
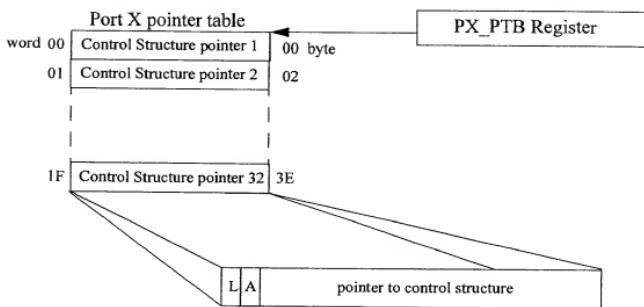


Fig. 3

Byte	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	Bit Number
0	reserved					srts			se	seq						0 Word	
2	gfc	vpi					vci					pti			c	1	
4	vci						pti						c		2		
6	hec				udf											3	
8	cell_count_statistic																4

c: clp  
se: srts\_enable

Fig. 4



L: Last valid pointer. When 1 indicates that the current pointer is the last valid pointer in the table  
A: Active. When 1 indicates the control structure is active

Fig. 5

Byte (hex)	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	Bit Number			
00	nmbr_of_tdm			nmbr_of_channéls			r	u								00 Field 0				
02	read_pointer		u	w	f	sits	seq									01 Field 1				
04	structure_length						u	mode								02 Field 2				
06	structure_boundary_ptr						se	ps	pe	p							03 Field 3			
08	current_cas			current_tdm													04 Field 4			
0A	last		ncas														05 Field 5			
0C	cell_count_statistic																06 Field 6			
0E	gfc	vpi			vci											07 Header 1				
10	vci			pti			c									08 Header 2				
12	hec			udf													09 Header 3			
14	circular_buffer_base_ptr1																0A			
16																				
18																				
1A																				
1C																				
1E																				
20																				
22																				
24																				
26																				
28																				
2A																				
2C																				
2E																				
30																				
32																				
34																				
36																				
38																				
3A																				
3C																				
3E																				
40																				
42																				
44																				
46																				
48																				
4A																				
4C																				
4E																				
50																				
52																				
54																				
56																				
58																				
5A																				
5C																				
5E																				
60																				
62																				
64																				
66																				
68																				
6A																				
6C																				
6E																				
70																				
72																				
74																				
76																				
78																				
7A																				
7C																				
7E																				
80																				

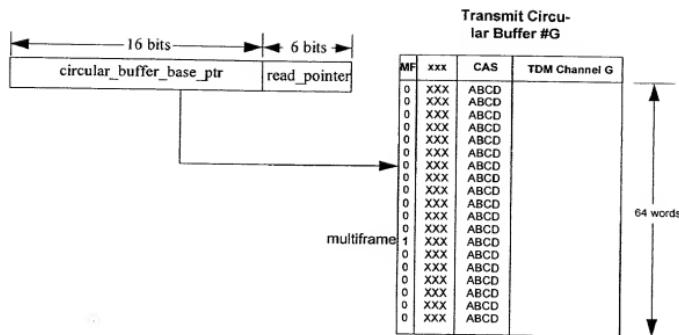


Fig. 7

## 4.5 DBCES Control Structure

Byte (hex)	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	Bit Number	Word (hex)
00	unused																00	Word (hex)
02		cell_count_statistic															01	
04	time_out		inactive_structure_timer														02	
06	gfc		vpi														03	
08		vci															04	
0A	hec		udf														05	
0C	nmbr_of_tdm		nmbr_of_channels														06	
0E	read_pointer		current_bm														07	
10		structure_lngth															08	
12		structure_boundary_ptr															09	
14		current_cas															0A	
16		last															0B	
18	unused		last_bm														0C	
1A		circular_buffer_base_ptr1															0D	
56		circular_buffer_base_ptr31															2B	
58	unused		bit_mask1														2C	
5A	unused		bit_mask2														2D	
5C	unused		bit_mask3														2E	
5E	unused		bit_mask4														2F	
60	nmbr_of_tdm		nmbr_of_channels														30	
62	read_pointer		current_bm														31	
64		structure_lngth															32	
66		structure_boundary_ptr															33	
68		current_cas															34	
6A		last															35	
6C	unused		last_bm														36	
6E		circular_buffer_base_ptr1															37	
AA		circular_buffer_base_ptr31															55	
AC	unused		bit_mask1														56	
AE	unused		bit_mask2														57	
B0	unused		bit_mask3														58	
B2	unused		bit_mask4														59	

Figure 19. DBCES control structure

Fig. 8